



X-1571 US
40/820,336

PATENT
Conf. No.: 8549

IN THE UNITED STATES PATENT OFFICE

Applicant: Robert O. Conn
Assignee: Xilinx, Inc.
Title: Coaxial Clock Tree for Programmable Logic Devices

Serial No.:	10/820,336	File Date:	04-08-04
Examiner:	Anh Q. Tran	Art Unit:	2819
Docket No.:	X-1571 US	Conf. No.:	8549

Mail Stop ISSUE FEE
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

RESPONSE TO NOTICE OF DRAWING INCONSISTENCY
WITH SPECIFICATION

Dear Sir:

In response to the Notice of drawing inconsistency with specification mailed from the Patent Office on October 6, 2005, please replace/substitute the following paragraph as indicated.

O.K. to Enter

ANH Q. TRAN
PRIMARY EXAMINER

[Signature]
1/9/06